advice POCKET

JN800J/JN800T

for NEC VR series

Specifications

Common Part	
Target Processor	VR4122/4131/4133/4181A VR5432/5500/5500A/7701
Operation Voltage	External: 2.5v – 3.6v, Internal: 1.5v – 2.7v, *) In case of N-wire 3.3v.
Operating Clock	150MHz – 400MHz
Memory Space	All memory space is released to a users system
Interrupts	All memory space is released to a users system
Endian	Little Endian/Big Endian
Target System I/F	[N-Wire Interface (38pin, 0.64mm pitch)] [N-Wire Trace Interface (38pin, 0.64mm pitch)] The connector on the target: Straight type, Plating, GND lead 1.4mm, Suit to 2-767004-2(AMP) Straight type, Palladium-Nickel, GND lead 2.74mm, Suit to 2-767054-1(AMP) Straight type, Palladium-Nickel, GND lead 3.51mm, Suit to 2-767061-1(AMP) [The conversion adaptor Interface (26pin, 1.27mm pitch)] The connector on the target: Suit to 8830E-026-170S/L(KEL).
Software Break	Point break: 1024 points by replacing instructions with software break Temporary break: 1 point (by using On-Chip Resource) Countable break: 1 point
Hardware Break	2 Points (max)
OCD Break	2 Point (Fetch Break/Data Access Break in the cash/user ROM area)
Flash Programming	Flash memory programmable with the standard commands (block erasing/programming) of JEDEC (compliant) & INTEL (equivalent) methods is supported.
Performance Feature	Only for JN800T model with VR5432, 5500, 5500A or 7701.

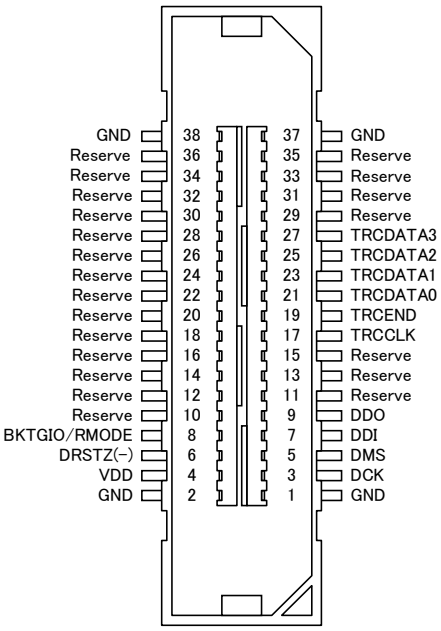
Other Specifications	
Host PC and OS	PC/AT Compatible, Microsoft Windows 98SE, Me, XP, NT4.0, 2000 USB(Full-speed)
Compiler	Green Hills C/C++ compiler

System Configuration

	Model	Details
1	JN800J	Main unit for JTAG Model, N-Wire Probe, Install Kit(Debugger, USB cable, Documents)
	JN800T	Main unit for Trace Model, N-Wire/Trace Probe, Install Kit(Debugger, USB cable, Documents)
	(Options)	
	External Cable	For the external trigger

N-Wire Probe/Trace Probe Pin Assignment (38pin)

Pin No.	Signal Name	Target I/O	Function	Pin No.	Signal Name	Target I/O	Function
38	GND	-	-	37	GND	-	-
36	(Reserved)	-	-	35	(Reserved)	-	-
34	(Reserved)	-	-	33	(Reserved)	-	-
32	(Reserved)	-	-	31	(Reserved)	-	-
30	(Reserved)	-	-	29	(Reserved)	-	-
28	(Reserved)	-	-	27	TRCDATA3	OUT	Trace Data 3
26	(Reserved)	-	-	25	TRCDATA2	OUT	Trace Data 2
24	(Reserved)	-	-	23	TRCDATA1	OUT	Trace Data 1
22	(Reserved)	-	-	21	TRCDATA0	OUT	Trace Data 0
20	(Reserved)	-	-	19	TRCEND	OUT	Trace Data End
18	(Reserved)	-	-	17	TRCCLK	OUT	Trace Clock
16	(Reserved)	-	-	15	(Reserved)	-	-
14	(Reserved)	-	-	13	(Reserved)	-	-
12	(Reserved)	-	-	11	(Reserved)	-	-
10	(Reserved)	-	-	9	DDO	OUT	JTAG Data
8	BKTGIO/ RMODE	IN/OUT	-	7	DDI	IN	JTAG Data
6	DRSTZ(-)	IN	JTAG Reset	5	DMS	IN	JTAG Trans. Mode
4	VDD	-	3.3v *1	3	DCK	IN	JTAG Clock
2	GND	-	-	1	GND	-	-



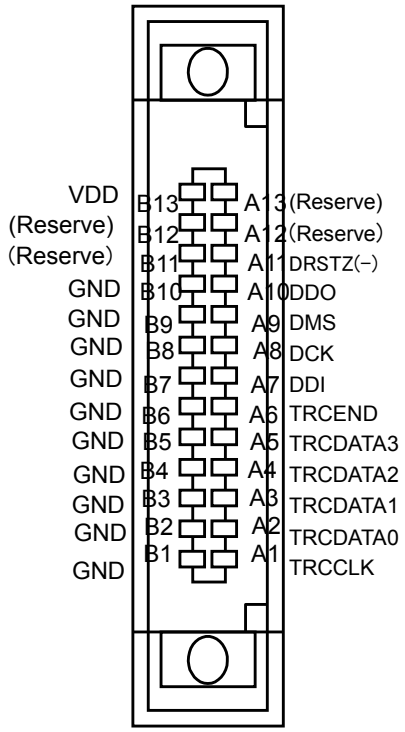
I/O direction is the input-output direction viewed from the user's system side.

- Input: JN800 advicePOCKET → User's system
- Output: User's system → JN800 advicePOCKET

*1 For monitoring the power supply of the user system.

The conversion adaptor Pin Assignment (38pin > 26pin)

Pin No.	Signal Name	Target I/O	Function	Pin No.	Signal Name	Target I/O	Function
B13	VDD	-	3.3v *1	A13	Reserve	-	-
B12	Reserve	-	-	A12	Reserve	-	-
B11	Reserve	-	-	A11	DRSTZ(-)	IN	JTAG Reset
B10	GND	-	-	A10	DDO	OUT	JTAG Data
B9	GND	-	-	A9	DMS	IN	JTAG Trans. Mode
B8	GND	-	-	A8	DCK	IN	JTAG Clock
B7	GND	-	-	A7	DDI	IN	JTAG Data
B6	GND	-	-	A6	TRCEND	OUT	Trace Data End
B5	GND	-	-	A5	TRCDATA3	OUT	Trace Data 3
B4	GND	-	-	A4	TRCDATA2	OUT	Trace Data 2
B3	GND	-	-	A3	TRCDATA1	OUT	Trace Data 1
B2	GND	-	-	A2	TRCDATA0	OUT	Trace Data 0
B1	GND	-	-	A1	TRCCLK	OUT	Trace Clock



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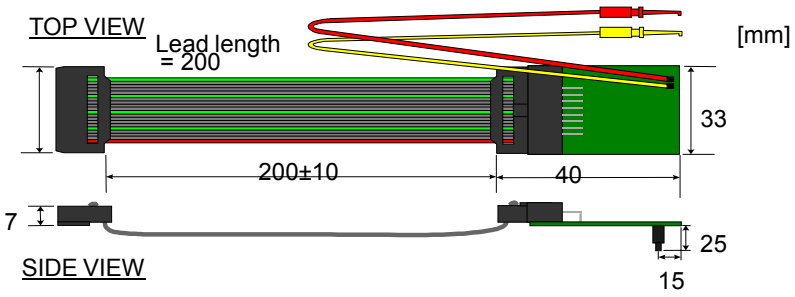
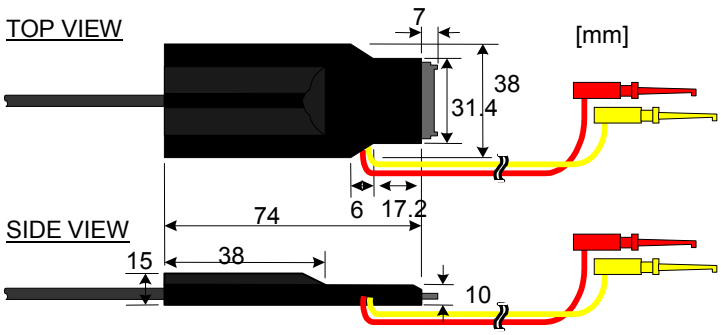
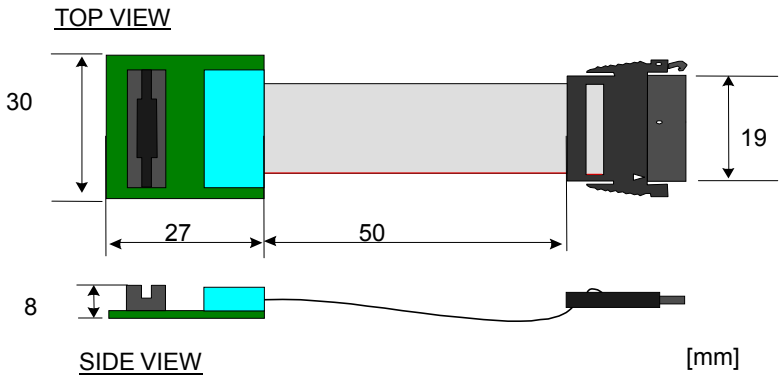
N-Wire probe dimension (38pin)



Trace probe dimension (38pin)



The conversion adaptor dimension (38pin > 26pin)



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