



JH601J/JH601T for SH-Mobile series

Specifications

| Common Part | |
|---------------------|--|
| Target Processor | SH7290(SH-Mobile 1), SH7294(SH-Mobile J), SH7300(SH-Mobile V) |
| Operation Voltage | VCCQ: 2.7 – 3.6v, VCC: 1.4 – 1.6v |
| Operating Clock | CPU: 133MHz(max), External Bus: [SH7290/7294]: 33MHz(max) [SH7300]: 66MHz(max) Peripheral Module: 33MHz(max) |
| Memory Space | All memory space is released to a users system |
| Interrupts | All memory space is released to a users system |
| Endian | Little Endian/Big Endian |
| Target System I/F | [H-UDI Interface (14pin, 2.54mm pitch)] The connector on the target: 7614-6002FL(Sumitomo 3M) [H-UDI + AUD Interface (36pin)] The connector on the target: DX10M-36S/DX10M-36SE/DX10GM-36SE(Hirose) |
| Software Break | Point break: 1024 points by replacing instructions with software break Temporary break: 1 point (by using On-Chip Resource) Countable break: 1 point |
| Hardware Break | 2 Points (max) |
| OCD Break | 1 Point (Fetch Break/Data Access Break in the cash/user ROM area) |
| Flash Programming | Flash memory programmable with the standard commands (block erasing/programming) of JEDEC (compliant) & INTEL (equivalent) methods is supported. |
| Performance Feature | Only for JH601T model. |

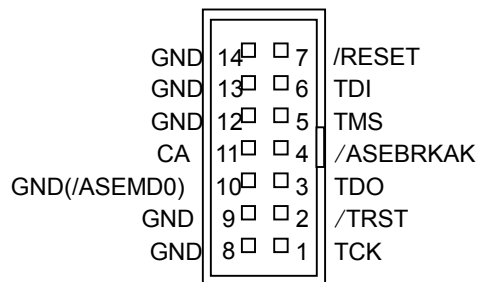
| Other Specifications | |
|----------------------|---|
| Host PC and OS | PC/AT Compatible, Microsoft Windows 98SE, Me, XP, NT4.0, 2000 USB(Full-speed) |
| Compiler | Renesas Technology C/C++ compiler Green Hills C/C++ compiler Gaio Technology C compiler |

System Configuration

| Model | Details |
|----------------|---|
| 1 JH601J | Main unit for JTAG Model, HUDI Probe, Install Kit(Debugger, USB cable, Documents) |
| JH601T | Main unit for Trace Model, HUDI/AUDProbe, Install Kit(Debugger, USB cable, Documents) |
| (Options) | |
| External Cable | For the external trigger |

H-UDI Pin Assignment

| Pin No. | Signal Name | notes | Pin No. | Signal Name | notes |
|---------|--------------|-------|---------|-------------|-------|
| 14 | GND | *4- | 7 | /RESET | *1 |
| 13 | GND | - | 6 | TDI | - |
| 12 | GND | - | 5 | TMS | - |
| 11 | CA | *3 | 4 | /ASEBRKAK | - |
| 10 | GND(/ASEMD0) | *2 | 3 | TDO | - |
| 9 | GND | - | 2 | /TRST | - |
| 8 | GND | - | 1 | TCK | - |



*1 Connect the signal to /RESETP of MPU.

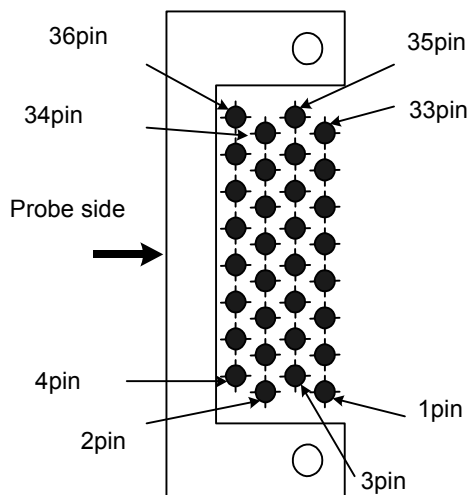
*2 By connecting /ASEMD0, MPU will be automatically ASE mode (ICE mode) when it is connecting to advicePOCKET.

*3 Be [+3.3v connection] or [N.C.] when the hardware standby (CA) is unavailable on the user system.

*4 By detecting the GND on the user system, the condition of the connecting to the user system is monitored.

Trace Probe Pin Assignment

| Pin No. | Signal Name | notes | Pin No. | Signal Name | notes |
|---------|-------------|-------|---------|-------------|-------|
| 36 | GND | - | 35 | NC | - |
| 34 | GND | - | 33 | GND | - |
| 32 | GND | - | 31 | /RESET | *3 |
| 30 | GND(ASEMD0) | *2- | 29 | CA | *1 |
| 28 | GND | - | 27 | /ASEBRKAK | - |
| 26 | GND | - | 25 | TDO | - |
| 24 | GND | - | 23 | TDI | - |
| 22 | GND | - | 21 | /TRST | - |
| 20 | GND | - | 19 | TMS | - |
| 18 | GND | - | 17 | TCK | - |
| 16 | GND | - | 15 | NC | - |
| 14 | GND | - | 13 | NC | - |
| 12 | GND | - | 11 | /AUDSYNC | - |
| 10 | GND | - | 9 | AUDATA3 | - |
| 8 | GND | - | 7 | AUDATA2 | - |
| 6 | GND | - | 5 | AUDATA1 | - |
| 4 | GND | - | 3 | AUDATA0 | - |
| 2 | GND | - | 1 | AUDCK | - |

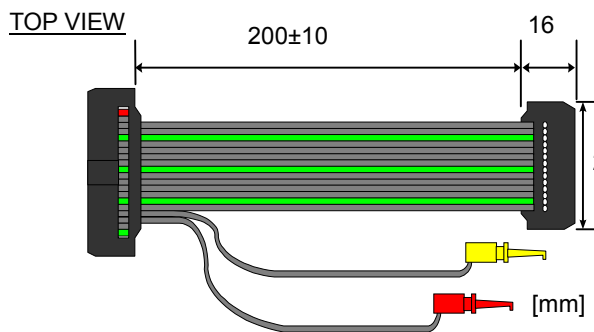


*1 Be [+3.3v connection] or [N.C.] when the hardware standby (CA) is unavailable on the user system.

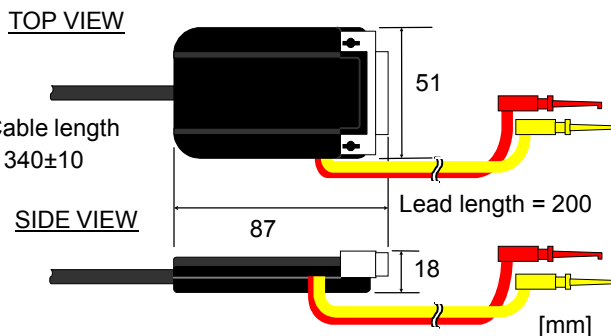
*2 Connect the signal to /RESETP of MPU.

*3 By connecting /ASEMD0, MPU will be automatically ASE mode (ICE mode) when it is connecting to advicePOCKET.

H-UDI Probe Dimension



Trace Probe Dimension



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