

YT800: advicePLUS for TX49/H2, H3 series

Specifications

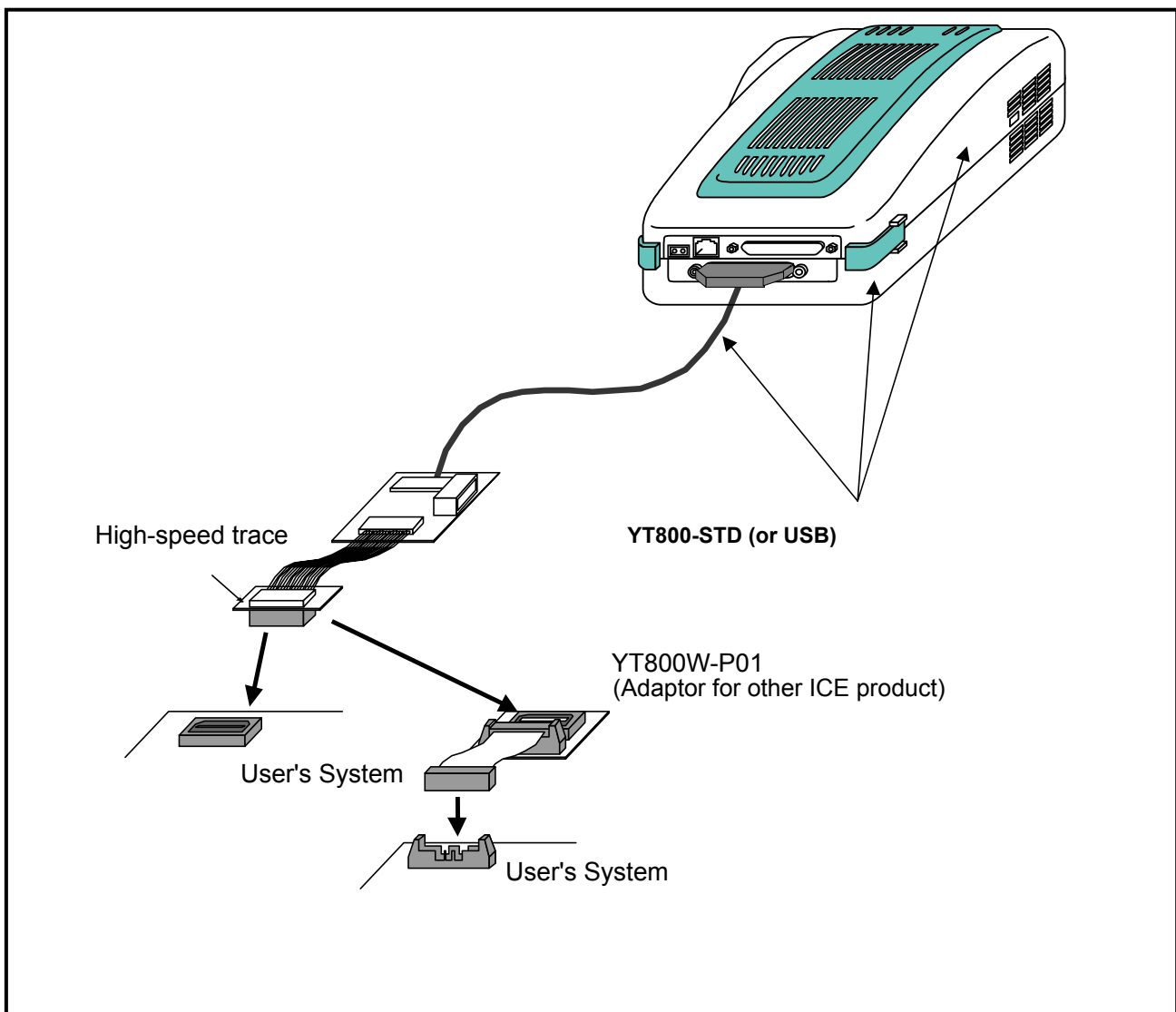
Common Part	
Target Processor	TX49/H2: TX4955A, 4956, 4927, 4925, 4926, 4923 TX49/H3: TX4937, 4938, 4955B TX49/L3: TX49/L3 Core ASIC
Operation Voltage	MPU (I/O) Voltage: Debug I/F Voltage: 3.3V
Operating Clock	333MHz (maximum)
Memory Space	All memory space made available to a user's system.
Interrupts	All interrupts made available to a user's system.
Target System I/F	TX49/H2: <ul style="list-style-type: none"> 38pin, 0.65mm pitch MIS-019-01-L-D (surface mount): SAMTEC 40pin, 1.27mm pitch (20pin, two row) Adaptor for YT600 probe FTSH-120-01-L-D-EJ-K (through hole): SAMTEC FTSH-120-01-L-DV-EJ-K (surface mount): SAMTEC TX49/H3: <ul style="list-style-type: none"> 38pin, 0.65mm pitch MIS-019-01-L-D(surface mount): SAMTEC
Internal ROM/RAM Emulation	ROM: Dedicated memory on probe/internal resource on chip (when exceeding 20MHz) RAM: Internal resource on chip
Breakpoint	1024 points by replacing instructions with software break Temporary break: 1 point (with 3 OCD breakpoints) Countable break: 1 point
Event	Event : 3 points (External Bus Trace Option: 15 Points) Trigger: 1 point External Bus Trace Option: 15 Points Event counter : 6 points Sequential switch : 4 levels Trigger Condition: The following AND/OR settings are available: Address : Match/Range Data : Match/Range/Don't Care Access Status : IFT/IRW/P/PFT/PR/PW/PRW/R/W/RW External Bus Trace Option Address : Match/not match/Range/Out of Range/Don't Care Data : Match/not match/Range/Out of Range/Don't Care
Trace	Size: 32K Samples Mode: Normal Mode/Multi Sample Mode Time Stamp: Resolution: 20ns/100ns
Flash Programming	Flash memory programmable with the standard commands (block erasing/programming) of JEDEC (compliant) & INTEL (equivalent) methods is supported.
Performance	Range to measure: To be set up with OR condition of Event Range to exclude from measurement: To be set up with OR condition of Event Time resolution: 20ns Time-over /Time-under break
Optional	External Bus Emulation: 4MB, 8MB, 16MB

Other Specifications	
Host PC	PC/AT Compatible, Microsoft Windows 98SE, Me, XP, NT4.0, 2000 10BASE-T/100BASE-TX, USB1.1
Debugger	microVIEW-PLUS (DT800)
Compiler	Green Hills c/c++ compiler Red Hat GNU Pro C compiler Wind River DIAB-SDS c/c++ compiler

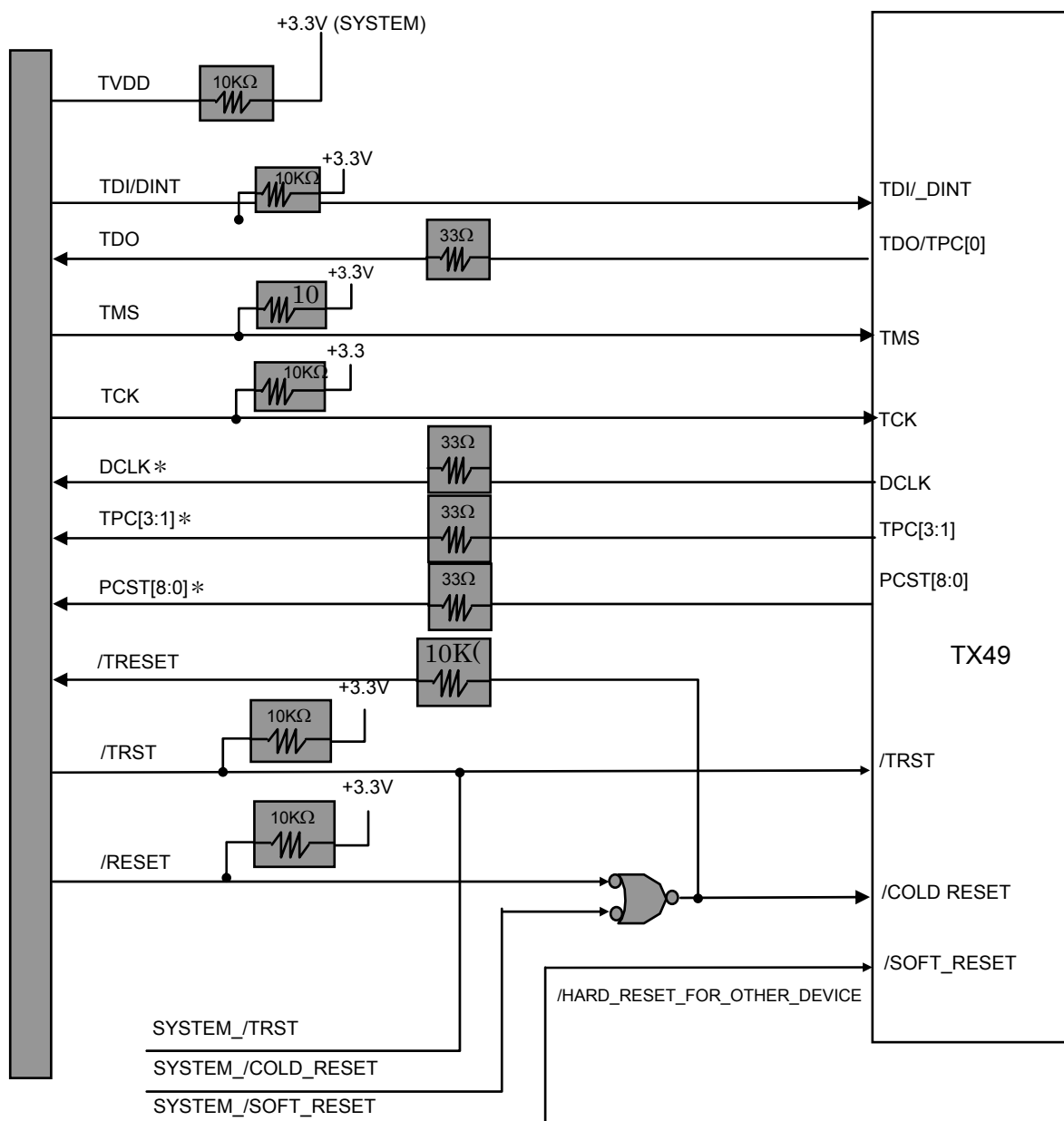
Product Configuration

Type	Description	
1	YT800-STD	Main Module with Ethernet interface/AC adaptor/EJTAG probe/adaptor
	YT800-USB	Main Module with USB interface/USB cable /AC adaptor/EJTAG probe/adaptor
2	YT800-EXB	External BUS Trace option
3	YT800-EM004	External ROM Emulation (4MB)
	YT800-EM008	External ROM Emulation (8MB)
	YT800-EM016	External ROM Emulation (16MB)
4	DT800	Debug software for TX49

Product Configuration (Figure)



User System Connection

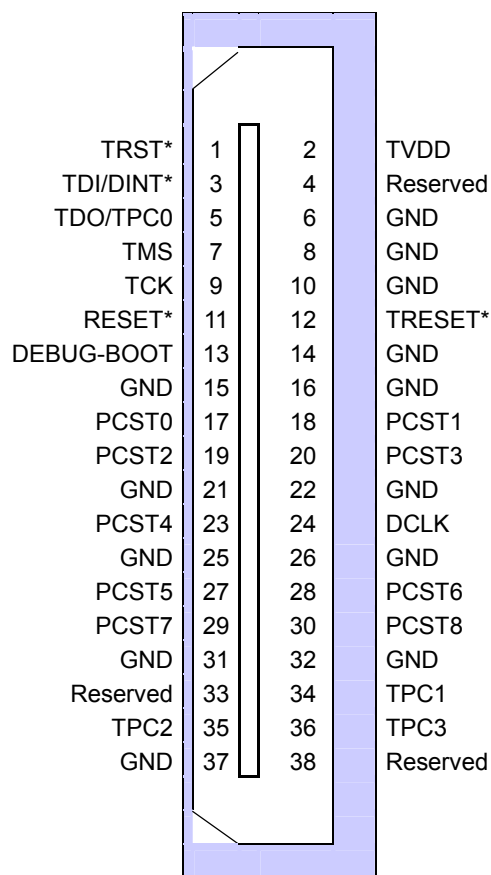


- For TVDD (No.2 pin) and _TRESET (No.12 pin), build a circuit configuration as shown above on a user's system.
- _TRST (No.1 pin) is open-collector output at the side of advicePLUS.
- advicePLUS side of _RESET has normal buffer output.
Signal target side should be logical OR with _COLDRESET.

Signal marked with the asterisk * is recommended to design signal wiring effectively so as to minimize length to reduce crossing signal.

User system connector

YT800 OCD I/F Pin Layout



TGT-side: MIS-019-01-L-D

ICE-Side: MIT-019-01-L-D

YT800 OCD I/F Pin Assignment

Pin#	Signal Name	I/O	Termination
1	TRST*	I	1K Ω pull-up
2	TVDD	-	Target VDD
3	TDI/DINT*	I	1K Ω pull-up
4	Reserved	I	NC
5	TDO/TPC0	O	33 Ω series
6	GND	-	-
7	TMS	I	1K Ω pull-up
8	GND	-	-
9	TCK	I	1K Ω pull-up
10	GND	-	-
11	RESET*	I	1K Ω pull-up
12	TRESET*	O	33 Ω series
13	DEBUG-BOOT	-	10K Ω pull-down
14	GND	-	-
15	GND	-	-
16	GND	-	-
17	PCST0	O	33 Ω series
18	PCST1	O	33 Ω series
19	PCST2	O	33 Ω series
20	PCST3	O	33 Ω series
21	GND	-	-
22	GND	-	-
23	PCST4	O	33 Ω series
24	DCLK	O	33 Ω series
25	GND	-	-
26	GND	-	-
27	PCST5	O	33 Ω series
28	PCST6	O	33 Ω series
29	PCST7	O	33 Ω series
30	PCST8	O	33 Ω series
31	GND	-	-
32	GND	-	-
33	Reserved	O	NC
34	TPC1	O	33 Ω series
35	TPC2	O	33 Ω series
36	TPC3	O	33 Ω series
37	GND	-	-
38	Reserved	O	NC

I/O is the direction viewed from a user's platform.

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