

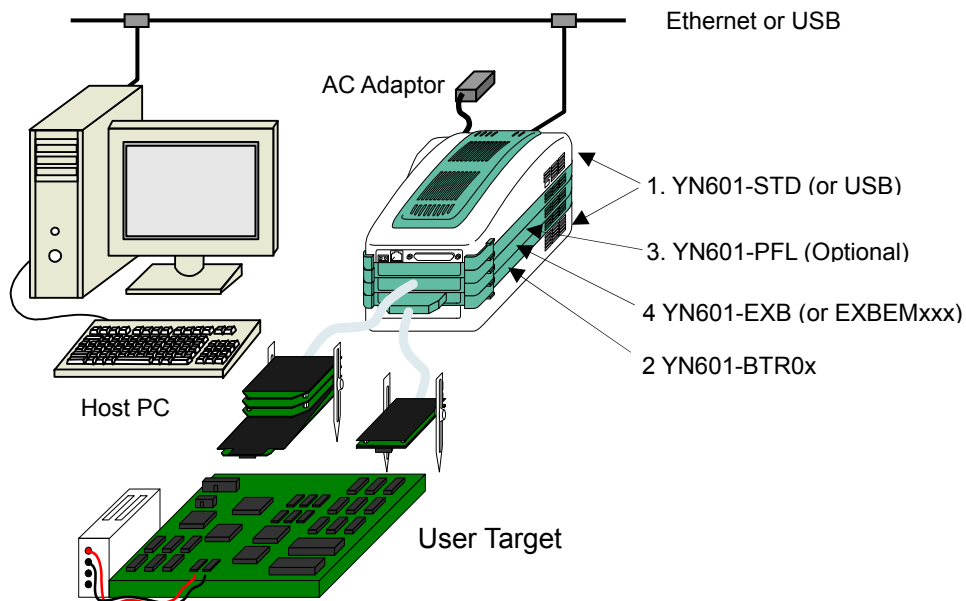
## YN601: advicePLUS for V850E/ME2 Specifications

Common Part	
Target Processor	V850E/ME2
Operation Voltage	Ivcc, PLLVcc: 1.4v to 1.65V
Operating Clock	150MHz (Maximum)
Memory Space	All memory space made available to a user's system
Interrupts	All interrupts made available to a user's system
Target System I/F	N-Wire Interface (38pin, 0.64mm pitch) , Socket on target: 2-767004-2 (AMP) N-Wire Interface (26pin, 1.27mm pitch), Socket on target: 8830E-026-170S or 8830E-026-170L (KEL) External Bus Trace, Emulation Interface (160pin, 0.5mm pitch), Socket on target: DG01FS-160-4110 (KEL)
Software Break	Point break: 1024 points (Maximum) 1 point for countable break
Hardware Break	1 point for temporary break 1 point for countable break 15 points for event break (Maximum)
OCD Break	1 point (Fetch Break/Data Access Break in the cache/user ROM area)
Flash Programming	Flash memory programmable with the standard commands (block erasing/programming) of JEDEC (compliant) & INTEL (equivalent) methods is supported.
Other Specifications	
Host PC	PC/AT Compatible, Microsoft Windows 98SE, Me, XP, NT4.0, 2000 10BASE-T/100BASE-TX, USB Full-Speed (Not available on NT4.0)
Debugger	microVIEW-PLUS (DN601)
Compiler	NEC Electronics C compiler Green Hills C/C++ compiler
RTOS	NEC Electronics Real-Time OS (RX850pro)

## System Configuration

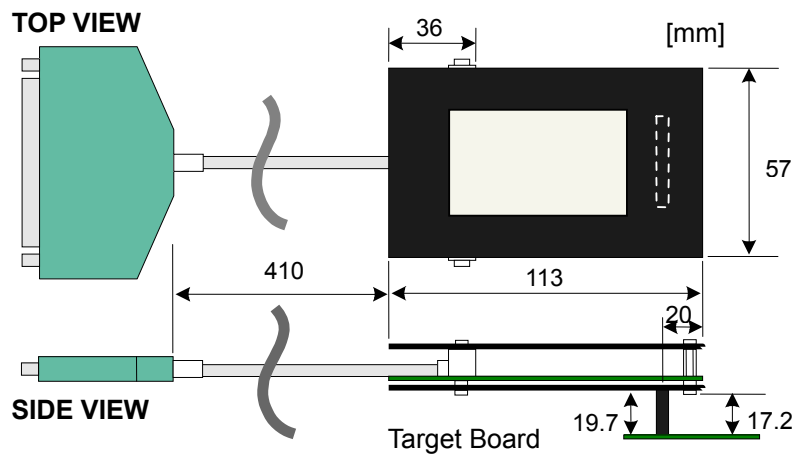
	Model	Description
1	YN601-STD	Main Module for V850E/ME2, Ethernet I/F, AC adaptor
	YN601-USB	Main Module for V850E/ME2, USB I/F and cable, AC adaptor
2	YN601-BTR01	N-Wire Trace Module, N-Wire Probe (MICTOR Connector 38pin 3.3V)
	YN601-BTR02	N-Wire Trace Module, N-Wire Probe (KEL Connector 26pin 3.3V)
3	YN601-PFL	Profile Module
4	YN601-EXB	SDRAM Trace Module
	YN601-EXBEM004	SDRAM Trace Module + External Bus Emulation Module, Ext. Bus Emulation Memory (4MB)
	YN601-EXBEM008	SDRAM Trace Module + External Bus Emulation Module, Ext. Bus Emulation Memory (8MB)
	YN601-EXBEM016	SDRAM Trace Module + External Bus Emulation Module, Ext. Bus Emulation Memory (16MB)
	YN601-EXBEM032	SDRAM Trace Module + External Bus Emulation Module, Ext. Bus Emulation Memory (32MB)
5	DN601	microVIEW-PLUS debugger for V850E/ME2
6	RR111/WIN	RTOS Option for NEC Electronics RTOS (RX850pro), Required for SDRAM Trace Option.

## System Connection Example

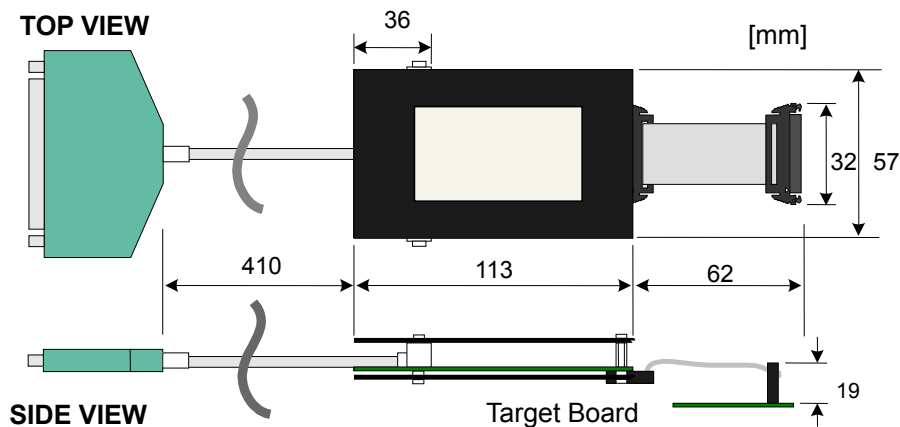


## Probe

### N-Wire Probe (MICTOR Connector 38pin) Pin Assignment



### N-Wire Probe (KEL Connector 28pin) Pin Assignment



## N-Wire Probe (MICTOR Connector 38pin) Pin Assignment

Pin No.	Signal Name	Target I/O	Target Termination	Pin No.	Signal Name	Target I/O	Target Termination
1	GND	-		20	TRCCE *1	Output	50KΩ Pull-down Resistor
2	GND	-		21	TRCDATA [0]	Output	22Ω Series Resistor
3	DCK	Input	4.7 KΩ Pull-up Resistor	22	TRCDATA [8] *1	Output	50KΩ Pull-down Resistor
4	VDD	+3.3v		23	TRCDATA [1]	Output	22Ω Series Resistor
5	DMS	Input	4.7 KΩ Pull-up Resistor	24	TRCDATA [9] *1	Output	50KΩ Pull-down Resistor
6	DRST	Input	50KΩ Pull-down Resistor	25	TRCDATA [2]	Output	22Ω Series Resistor
7	DDI	Input	4.7 KΩ Pull-up Resistor	26	TRCDATA [10] *1	Output	50KΩ Pull-down Resistor
8	PORT0_OUT *1	Open		27	TRCDATA [3]	Output	22Ω Series Resistor
9	DDO	Output	22Ω Series Resistor	28	TRCDATA [11] *1	Output	50KΩ Pull-down Resistor
10	PORT1_OUT *1	Open		29	TRCDATA [4]	Output	50KΩ Pull-down Resistor
11	NC	Open		30	TRCDATA [12] *1	Output	50KΩ Pull-down Resistor
12	PORT2_OUT *1	Open		31	TRCDATA [5]	Output	50KΩ Pull-down Resistor
13	NC	Open		32	TRCDATA [13] *1	Output	50KΩ Pull-down Resistor
14	PORT0_IN *1	Open		33	TRCDATA [6]	Output	50KΩ Pull-down Resistor
15	RESETZ *2	Output		34	TRCDATA [14] *1	Output	50KΩ Pull-down Resistor
16	PORT1_IN *1	Open		35	TRCDATA [7]	Output	50KΩ Pull-down Resistor
17	TRCCLK	Output	22Ω Series Resistor	36	TRCDATA [15] *1	Output	50KΩ Pull-down Resistor
18	PORT0_IN *1	Open		37	GND		
19	TRCEND	Output	22Ω Series Resistor	38	GND		

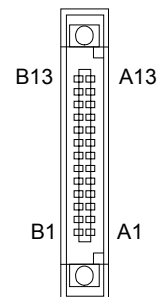
I/O direction is the input-output direction viewed from the user's system side.

\*1 For future enhancement

\*2 Reset signal of the user system.

## N-Wire Probe (KEL Connector 26pin) Pin Assignment

Pin No.	Signal Name	Target I/O	Target Termination	Pin No.	Signal Name	Target I/O	Target Termination
A1	TRCCLK	Output-		B1	GND	-	
A2	TRCDATA [0]	Output		B2	GND	-	
	TRCDATA [1]	Output	4.7 KΩ Pull-up Resistor	B3	GND	-	
	TRCDATA [2]	Output		B4	GND	-	
	TRCDATA [3]	Output	4.7 KΩ Pull-up Resistor	B5	GND	-	
	TRCEND	Output	50KΩ Pull-down Resistor	B6	GND	-	
	DDI	Input	4.7 KΩ Pull-up Resistor	B7	GND	-	
	DCK	Input		B8	GND	-	
	DMS	Input	22Ω Series Resistor	B8	GND	-	
	DDO	Output		B9	GND	-	
	DRST	Input		B10	PORT [2]	Output	
A12	PORT [0]	Open		B11	RESET (PORT [3]) *1	Output	
A13	PORT [1]	Open		B12	VDO	Output	



I/O direction is the input-output direction viewed from the user's system side.

\*1 Reset signal of the user system.

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